SPL 2012

Bento Gonçalves | March 20 to 23, 2012

VIII Southern Programmable Logic Conference

The Southern Programmable Logic Conference (SPL) is an international conference for researchers and developers interested in reconfigurable logic technology and its applications. The SPL2012 will take place in Bento Gonçalves, Brazil. The VIII edition continues the tradition of previous ones to become the austral meeting point for the worldwide community in the area.

TOPICS

Design Methodology
Low-Power Design
High-speed Techniques
Physical Design
Dynamic Reconfiguration
Interconnects and NoCs

EDA Tools
Logic and Architectural Synthesis
Modelling and Simulation Emulation

CAD for reconfigurable architectures
Reconfigurable hardware design languages
System-level design methods
Testing, verification and benchmarking
Hardware/software co-design

Platform-based Design
Embedded Processors
Custom Computers
Reconfigurable Multicore
IP Cores

Signal Processing
Computer Arithmetics
Digital Signal Processing
Adaptive Signal Processing
Image and Video Processing

FPGA in Education
Roadmap of programmable logic
Teaching Reconfigurable systems
Emerging Device technologies

Applications
Communications Networks
Artificial vision
Cryptography
Bioinformatics
Application acceleration
Rapid prototyping
High performance computing
Multimedia

Reliable Embedded Applications
Design verification and validation
Reliability and fault tolerance
FIT rates analysis
High reliability processor cores
Noise, radiation effects and EMC

CALL FOR PAPERS

SPL Conference Papers

Authors are invited to submit original and unpublished contributions as 6-page papers. The page limit includes tables, figures and references. Papers must be written in English. As previous editions, accepted papers will be published in the IEEE Xplore digital library.

Designer Forum Papers

Authors interested in contributing to the Designer Forum are invited to submit 4-page papers. This forum is held to give exposure to ongoing works and, due to the regional scope of this forum, papers can be written also in Spanish and Portuguese. Accepted contributions will be included in a separate proceeding but not in the IEEE library.

IMPORTANT DATES

SPL Submission Deadline	Designer Forum Submission Deadline	SPL and Designer Forum Camera-Ready Deadline
October 17, 2011	December 23, 2011	February 13, 2012
SPL Acceptance Notification	**Designer Forum Acceptance Notification**	**Conference**
December 12, 2011	January 23, 2012	March 20-23, 2012

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