SPL 2011

13 - 15 April 2011 | Córdoba, Argentina

VII Southern Programmable Logic Conference

SPL2011 is an international conference for researchers and developers interested in reconfigurable hardware technology and its applications. The VII edition will take place in Córdoba, the second-largest city in Argentina, and will be hosted at the National University of Córdoba, one of the oldest universities in America. Called the *Heartland of Argentina* for its location in the geographical center of the country, Córdoba has a strong colonial history blending with modern economic growth. It is considered the technological centre of Argentina where several international software and electronic industries are established.

TOPICS

Design Methodology
Low-Power Design
High-speed Techniques
Physical Design
Dynamic reconfiguration
Interconnects and NoCs

EDA Tools
Logic and Architectural Synthesis
Modelling and Simulation Emulation
CAD for reconfigurable architectures
Reconfigurable hardware design languages
System-level design methods
Testing, verification and benchmarking
Hardware/software co-design

Platform-based Design
Embedded Processors
Custom Computers
Reconfigurable Multicore Processors
IP Cores

Signal Processing
Computer Arithmetics
Digital Signal Processing
Adaptive Signal Processing

FPGA Architectures and Teaching
Emerging architectures and technologies
Roadmap of programmable logic
Reconfigurable hardware teaching

Applications
Communications Networks
Artificial vision
Cryptography
Bioinformatics
Application acceleration
Rapid prototyping
High performance computing

Reliable Embedded Applications
Design verification and validation
Reliability and fault tolerance
FIT rates analysis
High reliability processor cores
Noise, radiation effects and EMC

CALL FOR PAPERS

Conference Papers

Authors are invited to submit original and unpublished contributions as 6-page full papers This limit includes tables, figures and references. Papers must be written in English. As in previous editions, accepted papers will be published in the IEEE Xplore library.

Designer Forum Papers

Authors interested in contributing to the *Designer Forum* are invited to submit 4-page short papers. This forum is held to give exposure to ongoing works, academic experiences and industrial designs, and get feedback from experienced researchers and industrial partners. Due to the regional scope of this forum, papers can be written also in Spanish and Portuguese. Accepted contributions will be included in a separate proceedings volume but not in the IEEE proceedings. As such these will not appear in the IEEE Xplore library.

Full details of submission procedures are available on the SPL2011 website: **www.splconf.org**

IMPORTANT DATES

Paper Submission	Acceptance Notification	Camera Ready
15 October 2010	10 December 2010	7 February 2011

EXECUTIVE COMMITTEE

General Chairs
Jorge M. Finochietto
Gustavo Sutter

Local Chair
Carmen Rodriguez

Financial Chair
Ramiro Calderón

Technical Program Chairs
Mario Hueda
Khaled Benkrid

Designer Forum Chairs
Orlando Micolini
Pablo Recabarren

Tutorial Chair
Graciela Corral-Briones

Publicity Chairs
Eduardo Boemo
Edval Santos
Luciano Agostini
Valentin Obac Roda
Elias Todorovich

IEEE

CAS
IEEE CIRCUITS AND SYSTEMS SOCIETY

UNC Universidad Nacional de Córdoba